SMTInterpol

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Description

SMTInterpol is an SMT solver written in Java and available under LGPL v3. It supports the combination of the theories of uninterpreted functions, linear arithmetic over integers and reals, and arrays. Furthermore it can produce models, proofs, unsatisfiable cores, and interpolants. The solver reads input in SMT-LIB format. It includes parsers for DIMACS, AIGER, and SMT-LIB version 1.2 and 2.5.

The solver is based on the well-known DPLL(T)/CDCL framework [GHN+04]. The solver uses variants of standard algorithms for CNF conversion [PG86], congruence closure [NO05], Simplex [DdM06] and branch-and-cut for integer arithmetic [CH15a, DDA09]. The array decision procedure is based on weak equivalences [CH15b]. Theory combination is performed based on partial models produced by the theory solvers [dMB08].

This release uses the "sum-of-infeasibility" algorithm [KBD13] for linear arithmetic. The array theory was extended by constant arrays [HS19]. New in the current release is an experimental solver for quantified formulas. It is based on model-based quantifier instantiation, in particular, it solves the almost uninterpreted fragment [GdM09]. This approach is combined with ideas from conflict-based quantifier instantiation [RTdM14]. A distinguishing feature is that it does not use pattern-based E-matching at all, but uses the E-graph to identify potential conflict and unit clauses.

The main focus of SMTInterpol is the incremental track. This track simulates the typical application of SMTInterpol where a user asks multiple queries. The main focus of the development team of SMTInterpol is the interpolation engine [CH16, HS18]. This makes it useful as a backend for software verification tools. In particular, Ultimate Automizer[1] and CPAchecker[2], the winners of the SV-COMP 2016–2019, use SMTInterpol.

Competition Version

The version submitted to the SMT-COMP 2019 is an experimental release with the new solver for quantified formulas. The algorithms to generate proofs and models are not adapted to the new quantifier theory. Therefore, some features like unsat cores do not work yet. The solver is conservative and returns unknown for satisfiable formulas that are not in the supported fragment.

Further information about SMTInterpol can be found at

http://ultimate.informatik.uni-freiburg.de/smtinterpol/

The sources are available via GitHub

https://github.com/ultimate-pa/smtinterpol

[1] https://ultimate.informatik.uni-freiburg.de/
[2] https://cpachecker.sosy-lab.org/

Authors

The code was written by Jürgen Christ, Matthias Heizmann, Jochen Hoenicke, Alexander Nutz, Markus Pomrehn, Pascal Raiola, and Tanja Schindler.

Logics, Tracks and Magic Number

SMTInterpol participates in the single-query track, the incremental track, and the unsat core track. In the single-query and incremental track it supports all combinations of uninterpreted functions, linear arithmetic, and arrays: ALIA, AUFLIA, AUFLIRA, LIA, LRA, QF_ALIA, QF_AUFLIA, QF_AX, QF_IDL, QF_LIA, QF_LIRA, QF_LRA, QF_RDL, QF_UF, QF_UFIDL, QF_UFLIA, QF_UFLRA, UF, UFIDL, UFLIA, UFLRA. In the unsat core track, SMTInterpol participates only in the quantifier-free logics: QF_ALIA, QF_AUFLIA, QF_AX, QF_IDL, QF_LIA, QF_LIRA, QF_LRA, QF_RDL, QF_UF, QF_UFIDL, QF_UFLIA, QF_UFLRA.

Magic Number: 983 571 724

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