SMT-COMP 2015 entry: CVC4 v1.5-prerelease

CVC4 is a tool for determining the satisfiability of a first order formula modulo a first order theory (or a combination of such theories). It is the fourth in the Cooperating Validity Checker family of tools (CVC, CVC Lite, CVC3) but does not directly incorporate code from any previous version.

CVC4 is an open and extensible SMT engine. It can be used as a stand-alone tool or as a library. It has been designed to increase the performance and reduce the memory overhead of its predecessors, especially CVC3. It is written entirely in C++ and is released under an open-source license.

More information about CVC4 is available in [2] and at its website: `http://cvc4.cs.nyu.edu/`

Divisions in main track: all
Divisions in application track: all

Expected Performance: CVC4 has been carefully engineered for performance. We are competing in all divisions as a testament to how broadly applicable CVC4 is. We don't expect to perform particularly well in difference logic (as we aren't using a specialized difference logic solver) or in logics with nonlinear arithmetic, where support is planned but currently lacking. We expect it to perform competitively in most logics.

New Functionality We are entering two versions of the solvers, stable and experimental. The stable entry is similar to last year's entry with improvements in arrays and quantifiers. Experimental version has improvments in bit-vectors, non-linear integers, and further improvements to arrays and quantifiers.

Magic #: 44444

Authors

- Kshitij Bansal (NYU)
- Clark Barrett (NYU)
- François Bobot (CEA)
- Martin Brain (Oxford)
- Christopher Conway (Google)
- Morgan Deters (NYU)
- Liana Hadarean (Oxford)
- Dejan Jovanović (SRI)
- Timothy King (Verimag)
- Tianyi Liang (U. Iowa)
- Andrew Reynolds (EPFL)
- Cesare Tinelli (U. Iowa)

CVC4 incorporates code from MiniSAT for propositional reasoning. The experimental competition submission has support for CryptoMiniSat 4.2.0 [6], Glucose 4.0 [1] and Riss 5.0.1 [5] for bit-vector reasoning. The competition configuration submitted uses the CryptoMiniSat solver. CVC4 also uses ABC [3] for circuit simplification, and GLPK 4.52 [4] to solve hard arithmetic sub-problems.

References

[1] Gilles Audemard and Laurent Simon. Glucose: a solver that predicts learnt clauses quality. *SAT Competition*, pages 7–8, 2009.

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[3] Robert Brayton and Alan Mishchenko. ABC: An academic industrial-strength verification tool. In *Computer Aided Verification*, pages 24–40. Springer, 2010.

[4] Andrew Makhorin. GLPK (GNU linear programming kit), 2008.

[5] Norbert Manthey. Solver submission of riss 1.0 to the sat competition 2011. *SAT Competition*, 2011.

[6] Mate Soos. Cryptominisat 2.5. 0. *Solver description. SAT Race*, 2010.